

CLAIMS

We claim:

Sub A2
1. In a system having a data path functional unit having a functional unit data path width, a first memory system having a first data path width, and a second memory system having a data path width which is greater than the functional unit data path width and greater than the first data path width, the process including the steps of:

copying a first memory operand portion from the first memory system to the second memory system data, the first memory operand portion having the first data path width;

copying a second memory operand portion from the first memory system to the second memory system, the second memory operand portion having the first data path width and being catenated with the first memory operand portion; and

reading at least a portion of the catenated data which is greater than the first data path width.

2. The process of claim 1 further including the step of:

specifying a memory address indicia from which a plurality of data path widths of data can be read.

3. The process of claim 2 wherein memory address indicia includes at least a portion of first memory address and memory size and shape.

4. The process of claim 1 further including the step of checking the validity of the operand portion and, if valid, permitting a subsequent instruction to access the operand.

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